

JC841 U.S. PTO

A standard linear barcode is positioned vertically on the right side of the page.

12/15/00

ISSUE CLASSIFICATION

~~Best Available Copy~~

PATENT NUMBER

U.S. UTILITY Patent Application

	O.I.P.E. SEARCHED SERIALIZED INDEXED FILED ¹⁰ JUN SCANNED Q.A. Ann	PATENT DATE
---	---	--------------------

APPLICATION NO. 09/736282	CONT/PRIOR F	CLASS 714	SUBCLASS 719	ART UNIT 2133	EXAMINER <i>Chase</i>
------------------------------	-----------------	--------------	-----------------	------------------	--------------------------

APPLICANTS

Hidetoshi Fukui
Takeshi Hada
Masaki Nakanishi

Method of testing memory device, method of manufacturing memory device, apparatus for testing memory device, method of testing memory module, method of manufacturing memory module, apparatus for testing memory module and method of manufacturing computer

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____					
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE FEE	
				ISSUE BATCH NUMBER	
WARNING: Disclosure of this information to unauthorized persons is prohibited. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368.					

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by law. Disclosure outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

FILED WITH:

FILED WITH: DISK (CRF) FICHE CD-ROM
(Attached in pocket on right inside flap)